

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	35	((verif\$5 near4 data) same program\$5 same (shift\$3 near3 register\$1)) and ((memory near2 cell\$1) or (memory near2 element\$3) or (bit adj2 line\$1) or (word adj2 line\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/29 08:47
L3	34	((verif\$5 near4 data) same program\$5 same (shift\$3 near3 register\$1)) and ((memory near2 cell\$1) or (memory near2 element\$3) or (bit adj2 line\$1) or (word adj2 line\$1)) and select\$4	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/29 08:49
L4	28	((verif\$5 near4 data) same program\$5 same (shift\$3 near3 register\$1)) and ((memory near2 cell\$1) or (memory near2 element\$3) or (bit adj2 line\$1) or (word adj2 line\$1)) and select\$4 and (row\$1 or column\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/29 08:50
L5	26	((verif\$5 near4 data) same (shift\$3 near3 register\$1)) and ((memory near2 cell\$1) or (memory near2 element\$3) or (bit adj2 line\$1) or (word adj2 line\$1)) and select\$4 and (row\$1 or column\$1) and (program\$4 near2 (circuit or device))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/29 08:51
L9	15	((verif\$5 near4 data) same register\$1 same ((memory near2 cell\$1) or (memory near2 element\$3) or (bit adj2 line\$1) or (word adj2 line\$1)) same select\$4) and (program\$4 near2 (circuit or device))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/29 09:07
L10	15	((verif\$5 near4 data) same register\$1 same ((memory near2 cell\$1) or (memory near2 element\$3) or (bit adj2 line\$1) or (word adj2 line\$1)) same select\$4) and (program\$4 near2 (circuit or device))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/29 08:54
L11	188	(verif\$5 near4 data) same (program\$4 near2 (circuit or device))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/29 09:08
L12	131	(verif\$5 near2 data) same (program\$4 near2 (circuit or device))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/29 09:08
L13	63	(verif\$5 near2 data) near15 (program\$4 near2 (circuit or device))	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/29 09:08

L15	14	((verif\$5 near2 data) near15 (program\$4 near2 (circuit or device))) and (shift\$4 near3 register\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/12/29 09:09
L16	1	((verif\$5 near2 data) near15 (program\$4 near2 (circuit or device))) and (shift\$4 near3 register\$1)	US-PGPUB	OR	OFF	2005/12/29 09:10
L17	26	(verif\$5 near2 data) near15 (program\$4 near2 (circuit or device))	US-PGPUB	OR	OFF	2005/12/29 09:21
L18	0	ding-weiyang.in.	US-PGPUB	OR	OFF	2005/12/29 09:22
L19	0	vest-brad.in.	US-PGPUB	OR	OFF	2005/12/29 09:22


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IEEE STD IEEE Standard

## Select Article Information

- ☐ 1. **Fast in-place verification of data dependencies**  
 Li, L.;  
 Knowledge and Data Engineering, IEEE Transactions on  
 Volume 5, Issue 2, April 1993 Page(s):266 - 281  
 Digital Object Identifier 10.1109/69.219735  
[AbstractPlus](#) | Full Text: [PDF](#)(1320 KB) IEEE JNL
- ☐ 2. **Modular verification of data abstractions with shared realizations**  
 Ernst, G.W.; Hookway, R.J.; Ogden, W.F.;  
 Software Engineering, IEEE Transactions on  
 Volume 20, Issue 4, April 1994 Page(s):288 - 307  
 Digital Object Identifier 10.1109/32.277576  
[AbstractPlus](#) | Full Text: [PDF](#)(2120 KB) IEEE JNL
- ☐ 3. **BIND: a fine-grained attestation service for secure distributed systems**  
 Shi, E.; Perrig, A.; Van Doorn, L.;  
 Security and Privacy, 2005 IEEE Symposium on  
 8-11 May 2005 Page(s):154 - 168  
 Digital Object Identifier 10.1109/SP.2005.4  
[AbstractPlus](#) | Full Text: [PDF](#)(256 KB) IEEE CNF
- ☐ 4. **Performance comparison of several data structures for storing VLSI geor**  
 Grgek, M.; Branica, I.; Divkovic-Puksec, J.;  
 EUROCON 2003. Computer as a Tool. The IEEE Region 8  
 Volume 1, 22-24 Sept. 2003 Page(s):156 - 159 vol.1  
[AbstractPlus](#) | Full Text: [PDF](#)(362 KB) IEEE CNF
- ☐ 5. **Proceedings 2002 IEEE International Conference on Computer Design: VI Computers and Processors**  
 Computer Design: VLSI in Computers and Processors, 2002. Proceedings. 20th  
 International Conference on  
 16-18 Sept. 2002  
 Digital Object Identifier 10.1109/ICCD.2002.1106727  
[AbstractPlus](#) | Full Text: [PDF](#)(390 KB) IEEE CNF
- ☒ 6. **Design, verification, and validation of self-checking software component**  
 Geoghegan, S.J.; Avresky, D.;

Computers and Communications, 1996., Conference Proceedings of the 1996  
Annual International Phoenix Conference on  
27-29 March 1996 Page(s):420 - 426  
Digital Object Identifier 10.1109/PCCC.1996.493666

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IEEE STD IEEE Standard

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1. Design, verification, and validation of self-checking software component:
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- Geoghegan, S.J.; Avresky, D.;
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- Computers and Communications, 1996., Conference Proceedings of the 1996
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- Annual International Phoenix Conference on
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- 27-29 March 1996 Page(s):420 - 426
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